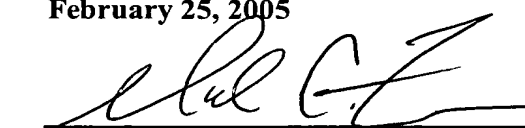


DFW

PATENT
Docket No. INTEL/18500

IN THE UNITED STATES PATENT
AND TRADEMARK OFFICE

Applicants:)	I hereby certify that this paper and the
)	documents referred to as enclosed
Nagendra et al.)	therewith are being deposited with the
)	United States Postal Service as first
U.S. Serial No.: 10/783,840)	class mail, postage prepaid, in an
)	envelope addressed to Commissioner
For: "Methods and Apparatus to)	for Patents, P.O. Box 1450,
Optimize Managed Application)	Alexandria, Virginia 22313-1450 on
Program Interfaces")	this date:
)	
Filed: February 20, 2004)	February 25, 2005
)	
Assignee: Intel Corporation)	
)	
Group Art Unit: 2122)	
)	Mark C. Zimmerman
Examiner: Not Yet Assigned)	Attorney for Applicant(s)
)	Registration No. 44,006

**CROSS-REFERENCE UNDER 37 C.F.R. §1.78(a)(2) TO RELATED
APPLICATIONS**

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Pursuant to 37 C.F.R. §1.78(a)(2), applicants hereby cross-reference the following related application, which is assigned to the same assignee as the present application:

(1) U.S. Patent Application No. 11/019,803 filed on December 21, 2004, entitled: "USING OPTIMIZED LIBRARIES TO IMPROVE PERFORMANCE OF DEPLOYED APPLICATION CODE AT RUNTIME" by Gururaj Nagendra and Stewart N. Taylor.

Respectfully submitted,

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By:

A handwritten signature in black ink, appearing to read 'M. C. Zimmerman', written over a horizontal line.

Mark C. Zimmerman
Registration No.: 44,006

February 25, 2005

Attorneys for Intel Corporation